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# SPECIFICATION

## VXT784BZE-01

- Preliminary Specification
- Final Specification



**CUSTOMER:**

<p><b>Made By:</b></p> <p><b>Checked By:</b></p> <p><b>Approved By:</b></p> <p><b>Quality:</b></p> <p><b>Date:</b></p> <p><b>Note:</b></p>
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<p><b>Approved By:</b></p>  <p><b>Date:</b></p>  <p><b>Note:</b></p>
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## Records of Revision

<b>DATE</b>	<b>REF.PAGE PARAGRAPH DRAWING No.</b>	<b>REVISED No.</b>	<b>SUMMARY</b>	<b>REMARK</b>
2018-02-26		V01	First Issue	

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# 1. General Specification

<b>Item</b>	<b>Contents</b>	<b>Unit</b>
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	67.60*205.78*4.75	MM
ACTIVE SIZE (W*H)	59.40*190.08	MM
NUMBER OF DOTS	400*1280	
DRIVER IC	EK79030AA	
INTERFACE TYPE	MIPI	
TOP POLARIZER TYPE	GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
BACKLIGHT TYPE	18-CHIP WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

## 2. Mechanical Drawing

**Pin Description Table:**

Pin	Symbol	Pin	Symbol
1	GND	21	NC
2	DIP	22	NC
3	DN	23	NC
4	GND	24	RSTB
5	DIP	25	NC
6	DN	26	NC
7	GND	27	GND
8	CLRP	28	K
9	CLKN	29	K
10	GND	30	GND
11	DIP	31	NC
12	DN	32	GND
13	GND	33	GND
14	DIP	34	NC
15	DN	35	A
16	GND	36	A
17	GND	37	GND
18	VCC-1V8	38	VDD-3V3
19	VCC-1V8	39	VDD-3V3
20	NC	40	NC

**Notes:**

- General Tolerance:±0.2
- Dimensional tolerance:±0.1
- Recommended cushion open area should be less than module V.A
- Recommended cushion adherent area: TP V.A+1.6mm
- RoHS must be compliant.

**Specifications Table:**

Display Type	TFT
Optimum Viewing Direction	NORMALLY BLACK, TRANSMISSIVE
Surface Treatment	Glare
LCD Driver IC	EK79030AA
Logic Voltage	V <sub>G</sub> =3.3V
Operating Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Interface	MIPI
Backlight	18-LED WHITE
Surface luminance	TBD
White	X/Y

**Dimensions and Features:**

- Top view: 67.60±0.2(LCM O.D.), 64.80(LCD O.D.), 59.40(LCD A.A.), 33.80, 7.84 TFT, 400(RGB)\*1280, 102.89, 7.85, 4.85, 199.88(LCD O.D.), 205.78±0.2(LCM O.D.), 40.24±0.5
- Side view: 1.75±0.2, 7.5 MAX, 40.78±0.5
- Bottom view: 37.96±0.5
- Mounting hole: 6.00

**Pinout Diagram:**

```

    graph TD
      K --- K1
      K --- K2
      K --- K3
      K --- K4
      K --- K5
      K --- K6
      K --- K7
      K --- K8
      K --- K9
      K --- K10
      K --- K11
      K --- K12
      K --- K13
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      K --- K33
      K --- K34
      K --- K35
      K --- K36
      K --- K37
      K --- K38
      K --- K39
      K --- K40
  
```

**Customer Approval:**

DATE: 2017.12.01A  
SIGN: \_\_\_\_\_  
APPROVAL: \_\_\_\_\_

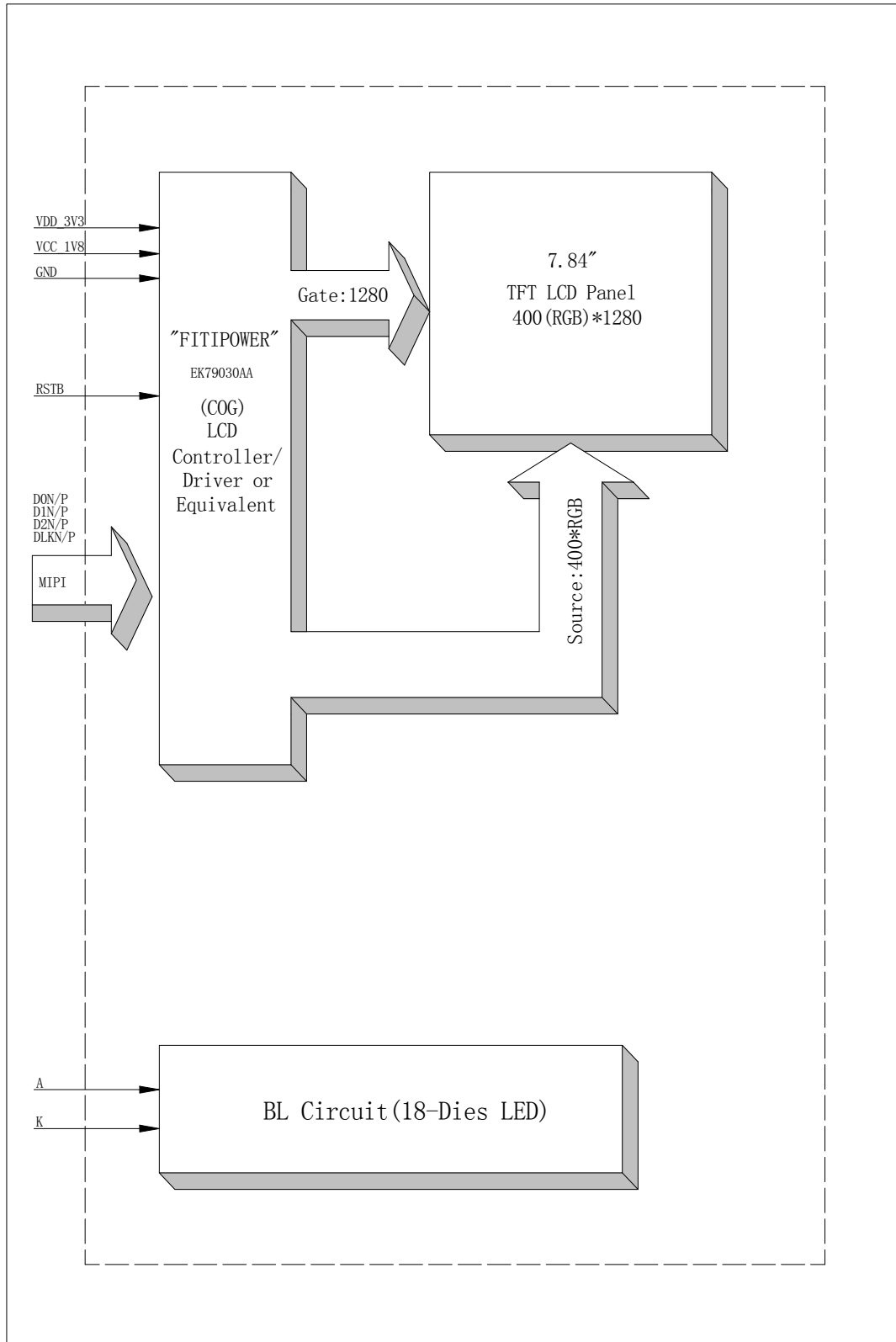
**Module Specifications:**

DRAWING NO. **VXT784BZE-01**

UNIT: mm SCALE: FIT SHEET 1 OF 1

**Manufacturer:** Victronix Tecenstar INT'L Co., LTD

### 3. Block Diagram



## 4. Interface Pin Function

Pin No.	Symbol	Description
1	GND	Power ground
2	D0P	MIPI data negative signal
3	D0N	MIPI data negative signal
4	GND	Power ground
5	D1P	MIPI data negative signal
6	D1N	MIPI data negative signal
7	GND	Power ground
8	DLKP	MIPI clock negative signal
9	DLKN	MIPI clock negative signal
10	GND	Power ground
11	D2P	MIPI data negative signal
12	D2N	MIPI data negative signal
13	GND	Power ground
14	D3P	MIPI data negative signal
15	D3N	MIPI data negative signal
16	GND	Power ground
17	GND	Power ground
18	VCC-1V8	I/O Power supply(1.8v)
19	VCC-1V8	I/O Power supply(1.8v)
20	NC	No connection
21	NC	No connection
22	NC	No connection
23	NC	No connection
24	RSTB	Reset pin.
25	NC	No connection
26	NC	No connection
27	GND	Power ground
28	K	Cathode of LED backlight
29	K	Cathode of LED backlight
30	GND	Power ground
31	NC	No connection
32	GND	Power ground
33	GND	Power ground
34	NC	No connection
35	A	Anode of LED backlight
36	A	Anode of LED backlight
37	GND	Power ground
38	VDD_3V3	Power supply(3.3v)
39	VDD_3V3	Power supply(3.3v)
40	NC	No connection

## 5. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	VCC	-0.3	5	V
Supply voltage for analog	VDD	-0.3	5	V
Power supply	VGH	12	18	V
Power supply	VGL	-14	-8	V
Supply current (One LED)	I <sub>LED</sub>		30	mA
Operating temperature	T <sub>OP</sub>	-20	+70	°C
Storage temperature	T <sub>ST</sub>	-30	+80	°C

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.



## 6. Electrical Characteristics

### 6.1 Input Power

Item	Symbol	Min	Typ.	Max	Unit	Applicable terminal
Supply voltage for logic	VCC	-	1.8	-	V	
Supply voltage for analog	VDD	-	3.3	3.6	V	
Input Voltage	V <sub>IL</sub>	0	-	0.2VDD	V	
	V <sub>IH</sub>	0.8 VDD	-	VDD		
Input leakage Current	I <sub>LKG</sub>	-		-	μA	

### 6.2 Backlight Driving Conditions

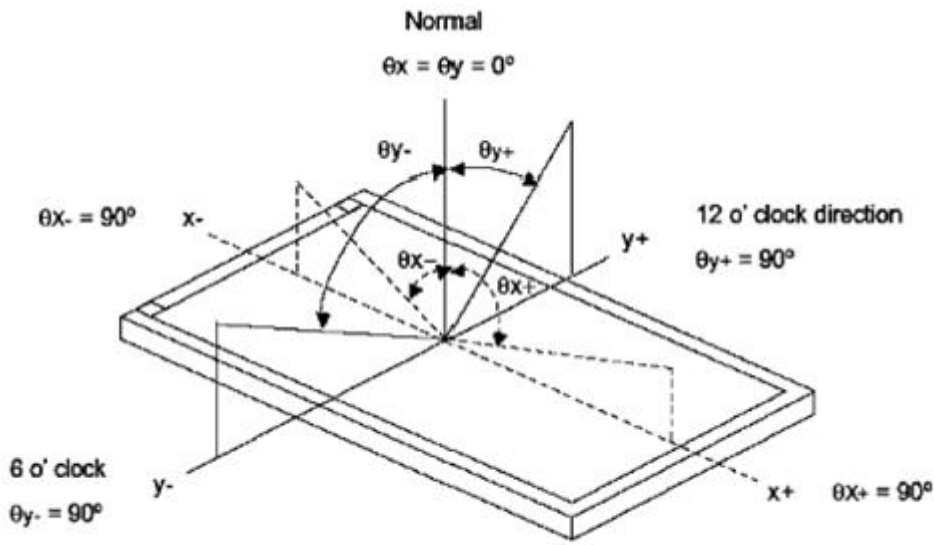
Item	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V <sub>F</sub>	16.8	18	19.2	V	I <sub>L</sub> =60mA
Current for LED Backlight	I <sub>L</sub>		60		mA	
Power Consumption	P		1.08		W	
LED Life Time		30,000	50,000		Hr	Note

Note: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25°C

## 7. Optical Characteristics

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
			MIN	TYP.	MAX		
Luminance	L	$I_L = 60\text{mA}$	320	400	480	$\text{Cd/m}^2$	
Contrast Ratio	CR	$\theta = 0^\circ$	700	900			
Response Time	$T_{\text{ON}}$	$25^\circ\text{C}$		30	40	ms	
	$T_{\text{OFF}}$						
CIE Color Coordinate	Red	$X_R$	Viewing normal angle	0.621	0.641	0.661	
		$Y_R$		0.327	0.347	0.367	
	Green	$X_G$		0.305	0.325	0.345	
		$Y_G$		0.599	0.619	0.639	
	Blue	$X_B$		0.120	0.140	0.160	
		$Y_B$		0.095	0.115	0.135	
	White	$X_W$		0.306	0.326	0.346	
		$Y_W$		0.352	0.372	0.392	
Viewing Angle	Hor.	$\theta_{X+}$	$\text{CR} \geq 10$		80	Degree	
		$\theta_{X-}$			80		
	Ver.	$\theta_{Y+}$			80		
		$\theta_{Y-}$			80		
Uniformity	Un		-	70	-	%	

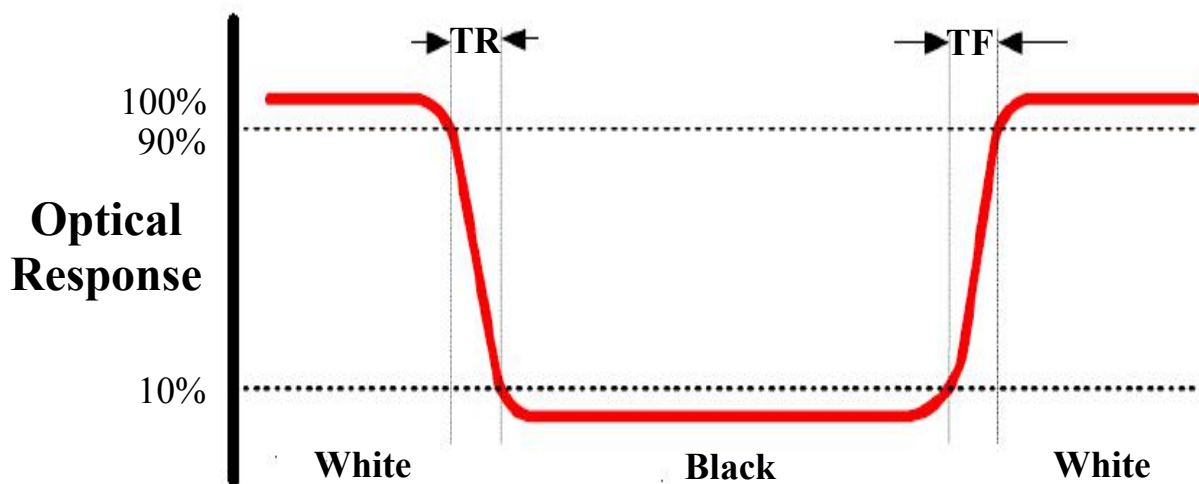
**Note 1: Definition of Viewing Angle  $\theta_x$  and  $\theta_y$ :**



**Note 2: Definition of contrast ratio CR:**

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

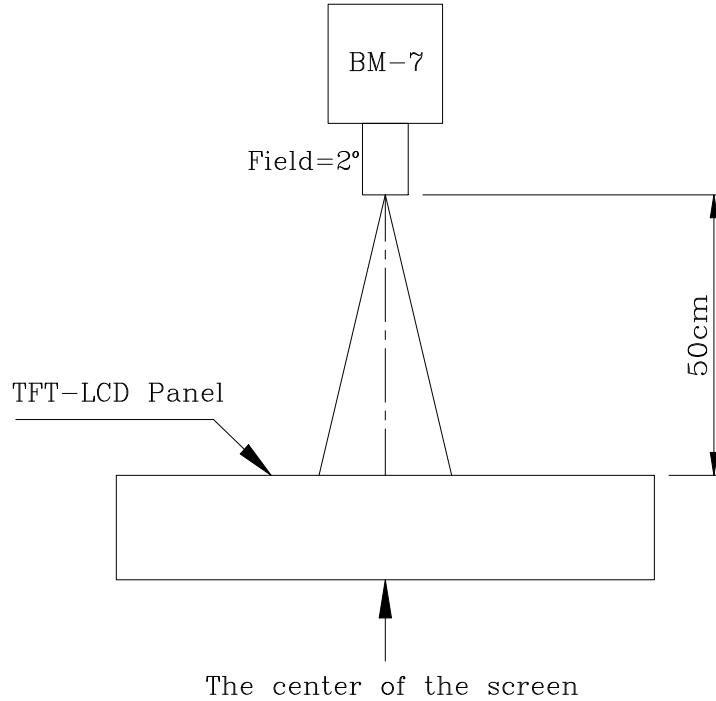
**Note 3: Definition of Response Time ( $T_r, T_f$ )**



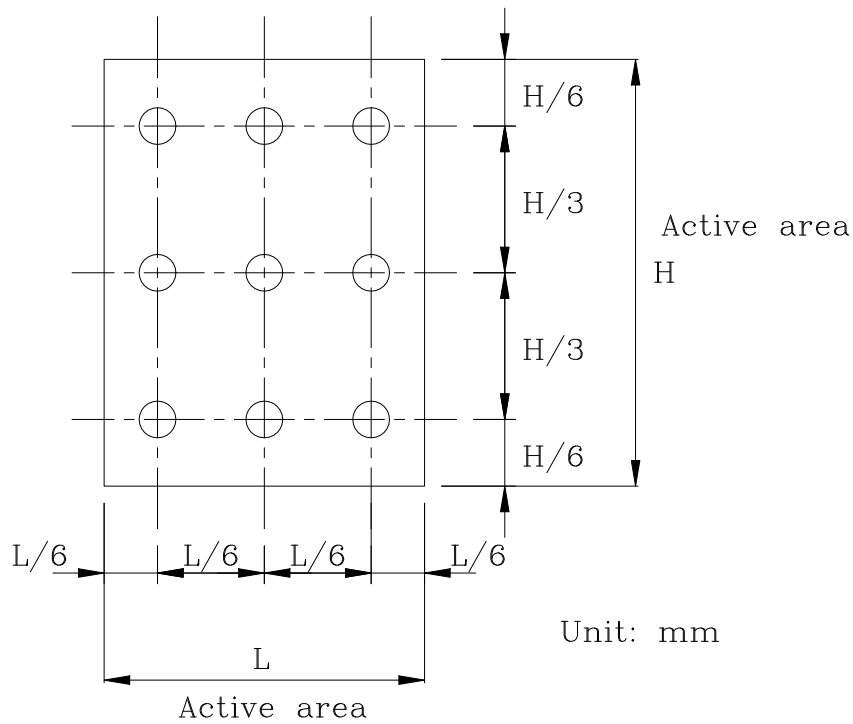
**Note 4: Definition of Luminance**

**①The Brightness Test Equipment Setup**

Field=2° (As measuring “black” image, field=2° is the best testing condition)



**②The Brightness Test Point Setup**



## 8. Timing Characteristics

### 8.1 MIPI Input Timing Table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
MIPI (4 Lane) @Frame rate=60Hz			349		Mbps
MIPI (3 Lane) @Frame rate=60Hz			465		Mbps
DCLK frequency @Frame rate=60Hz	$F_{DCLK}$		58.2		MHz
HSYNC period time	$T_H$		744		DCLK
Horizontal display area	$T_{HD}$		400		DCLK
HSYNC pulse width	$T_{HPW}$		24	-	DCLK
HSYNC back porch	$T_{HBP}$		160	-	DCLK
HSYNC front porch	$T_{FBP}$		160	-	DCLK
VSYNC period time	$T_V$		1304		H
Vertical display area	$T_{VD}$		1280		H
VSYNC pulse width	$T_{VPW}$		2	-	H
VSYNC back porch	$T_{VBP}$		10	-	H
VSYNC front porch	$T_{VFP}$		12	-	H

## 8.2 MIPI AC Characteristics

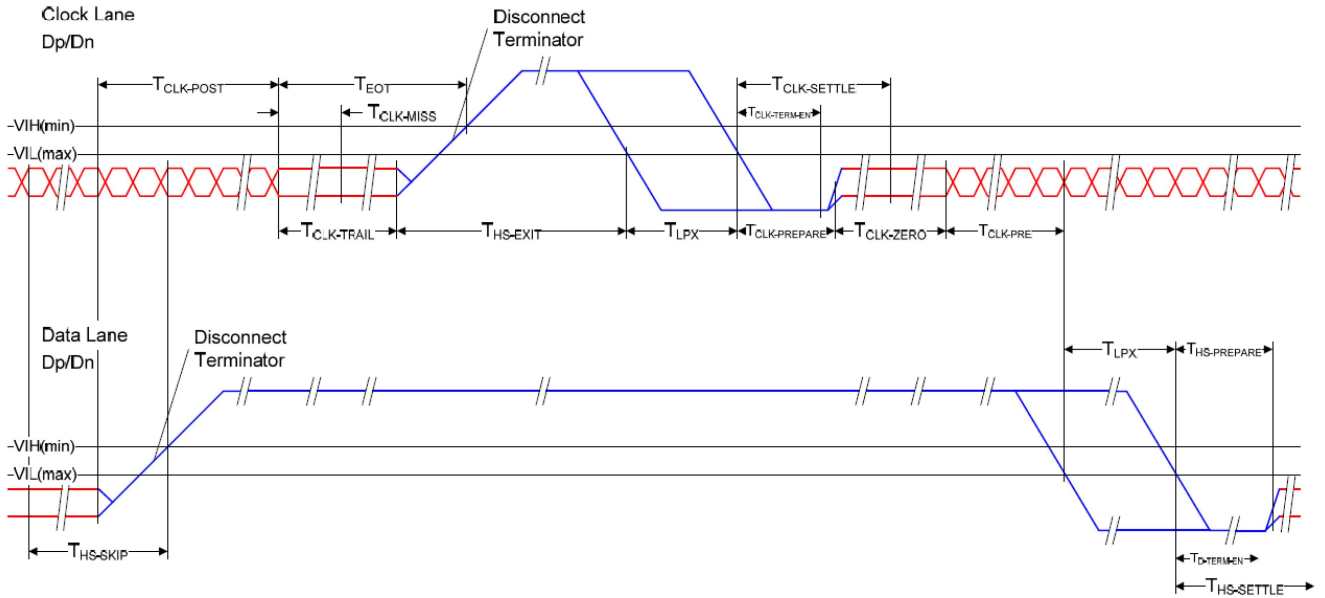


Figure 13.1: Switching the clock lane between clock transmission and low-power mode

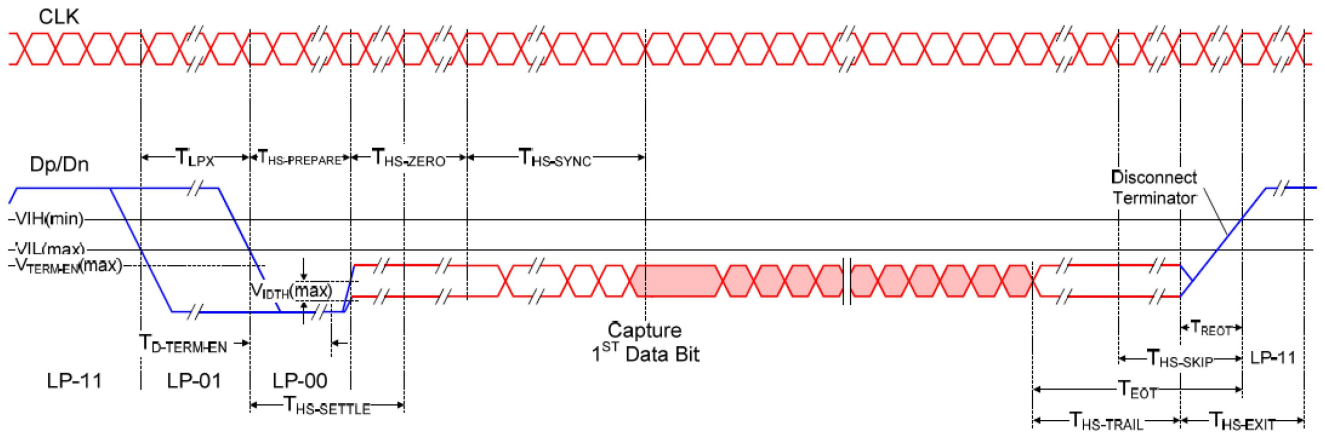


Figure 13.2: Timing of high-speed data transmission in bursts

## 9. Standard Specification for Reliability

### 9.1 Standard Specification for Reliability of LCD Module

Item	Test Conditions	Remark
High temperature storage	Ta=80°C 240hrs	NOTE1 , NOTE4
Low temperature storage	Ta=-30°C 240hrs	NOTE1 , NOTE4
High temperature operation	Ta=70°C 240hrs	NOTE2 , NOTE4
Low temperature operation	Ta=-20°C 240hrs	NOTE2 , NOTE4
Operate at high temperature and humidity	+60°C, 90%RH 240hrs	NOTE4
Thermal Shock	-20°C/30min~+60°C/30min for a total 100 cycles, start with cold temperature and end with high temperature.	NOTE4
Vibration Test	Frequency range:10~55HZ Stroke:1.5mm Swap:10HZ~55HZ~10HZ 2 hours of each direction of X.Y. Z (6 hours for total)	
Mechanical shock	200G 2ms, ±X, ±Y, ±Z 3 times for each direction	
Package vibration test	Random vibration :1.5G*G/HZ from 10-500 HZ,-6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)	
Packing drop test	Height:60cm 1 corner ,3 edges ,6 surfaces	
Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times	
	Contact: ±2KV 150pF/330Ω 5 times	

Note 1: Ta is the ambient temperature of samples.

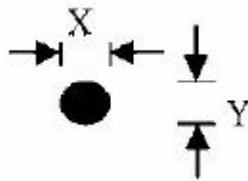
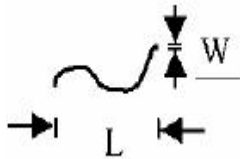
Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function.

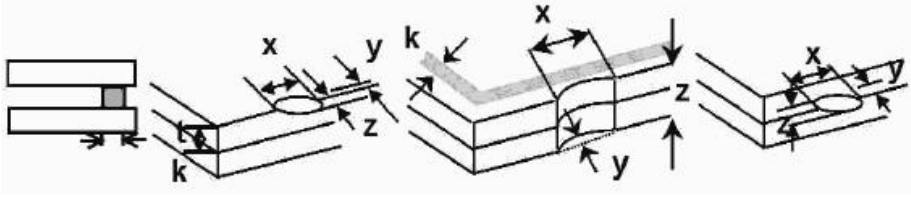
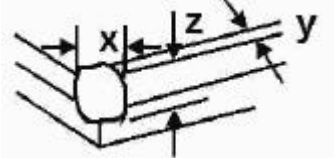
After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

## 9.2 Inspection Specification

NO.	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	0.65												
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White spots can't be accepted .Black or color spots on display $\leq$ 0.20mm, no more than 3 spots. 2.2 Densely spaced: No more than each three black spots within 3mm.	2.5												
03	LCD and Touch Panel black spots, white spots, contamination (non – display)	3.1 Round type: As following drawing $\Phi = (X+Y) / 2$  <table border="1" data-bbox="821 1131 1353 1400"> <thead> <tr> <th>Size(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.20</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.25</math></td> <td>Rejection</td> </tr> <tr> <td><math>0.25 &lt; \Phi \leq 0.30</math></td> <td>Rejection</td> </tr> <tr> <td><math>0.30 &lt; \Phi</math></td> <td>Rejection</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two spots within 3mm.</p>	Size(mm)	Acceptable Q'ty	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.25$	Rejection	$0.25 < \Phi \leq 0.30$	Rejection	$0.30 < \Phi$	Rejection	2.5
		Size(mm)	Acceptable Q'ty												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	Accept no dense														
$0.20 < \Phi \leq 0.25$	Rejection														
$0.25 < \Phi \leq 0.30$	Rejection														
$0.30 < \Phi$	Rejection														
3.2 Line type: (As following drawing)  <table border="1" data-bbox="726 1534 1353 1803"> <thead> <tr> <th>Length(mm)</th> <th>Width(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \leq 0.02</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>L \leq 3.0</math></td> <td><math>0.02 &lt; W \leq 0.05</math></td> <td rowspan="2">2</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.03 &lt; W \leq 0.08</math></td> </tr> <tr> <td>---</td> <td><math>0.08 &lt; W</math></td> <td>Rejection</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two lines within 3mm.</p>	Length(mm)	Width(mm)	Acceptable Q'ty	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.05$	2	$L \leq 2.5$	$0.03 < W \leq 0.08$	---	$0.08 < W$	Rejection	2.5
Length(mm)	Width(mm)	Acceptable Q'ty													
---	$W \leq 0.02$	Accept no dense													
$L \leq 3.0$	$0.02 < W \leq 0.05$	2													
$L \leq 2.5$	$0.03 < W \leq 0.08$														
---	$0.08 < W$	Rejection													



NO.	Item	Criterion	AQL			
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction	Size $\Phi$ (mm)	Acceptable Q'ty	2.5	
			$\Phi \leq 0.20$	Accept no dense		
			$0.20 < \Phi \leq 0.50$	Rejection		
			$0.50 < \Phi \leq 1.00$	Rejection		
			$1.00 < \Phi$	Rejection		
			Total Q'ty	2		
05	Scratches	Follow NO.3 -2 Line Type.				
06	Chipped glass	<p>Symbols:  x: Chip length    y: Chip width    z: Chip thickness  k: Seal width    t: Glass thickness    a: LCD side length  L: Electrode pad length</p> <p>6.1 General glass chip:  6.1.1 Chip on panel surface and crack between panels:</p>	2.5			
						
		<table border="1"> <tr> <td data-bbox="395 1189 660 1227">z: Chip thickness</td> <td data-bbox="660 1189 975 1227">y: Chip width</td> <td data-bbox="975 1189 1214 1227">x: Chip length</td> </tr> </table>		z: Chip thickness	y: Chip width	x: Chip length
		z: Chip thickness		y: Chip width	x: Chip length	
		<table border="1"> <tr> <td data-bbox="395 1234 660 1294"><math>Z \leq 1/2t</math></td> <td data-bbox="660 1234 975 1294">Not over viewing area</td> <td data-bbox="975 1234 1214 1294"><math>x \leq 1/8a</math></td> </tr> </table>		$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$
		$Z \leq 1/2t$		Not over viewing area	$x \leq 1/8a$	
<table border="1"> <tr> <td data-bbox="395 1301 660 1361"><math>1/2t &lt; z \leq 2t</math></td> <td data-bbox="660 1301 975 1361">Not exceed 1/3k</td> <td data-bbox="975 1301 1214 1361"><math>x \leq 1/8a</math></td> </tr> </table>	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$				
<p>⊙ Unit: mm  ⊙ If there are 2 or more chips, x is the total length of each chip</p>						
<p>6.1.2 Corner crack:</p>						
						
<table border="1"> <tr> <td data-bbox="395 1682 660 1720">z: Chip thickness</td> <td data-bbox="660 1682 975 1720">y: Chip width</td> <td data-bbox="975 1682 1214 1720">x: Chip length</td> </tr> </table>	z: Chip thickness	y: Chip width	x: Chip length			
z: Chip thickness	y: Chip width	x: Chip length				
<table border="1"> <tr> <td data-bbox="395 1727 660 1787"><math>Z \leq 1/2t</math></td> <td data-bbox="660 1727 975 1787">Not over viewing area</td> <td data-bbox="975 1727 1214 1787"><math>x \leq 1/8a</math></td> </tr> </table>	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$				
<table border="1"> <tr> <td data-bbox="395 1794 660 1854"><math>1/2t &lt; z \leq 2t</math></td> <td data-bbox="660 1794 975 1854">Not exceed 1/3k</td> <td data-bbox="975 1794 1214 1854"><math>x \leq 1/8a</math></td> </tr> </table>	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$				
<p>⊙ Unit: mm  ⊙ If there are 2 or more chips, x is the total length of each chip</p>						

## 10. General Precautions

### 10.1. Safety

- Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

### 10.2. Handling

- The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- To avoid contamination on the display surface, do not touch the module surface with bare hands.
- Keep a space so that the LCD panels do not touch other components.
- Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
- Do not leave module in direct sunlight to avoid malfunction of the ICs.

### 10.3. Static Electricity

- Be sure to ground module before turning on power or operating module.
- Do not apply voltage which exceeds the absolute maximum rating value.

### 10.4. Storage

- Store the module in a dark room where must keep at  $25\pm 10^{\circ}\text{C}$  and 65%RH or less.
- Do not store the module in surroundings containing organic solvent or corrosive gas.
- Store the module in an anti-electrostatic container or bag.

### 10.5. Cleaning

- Do not wipe the polarizer with dry cloth. It might cause scratch.
- Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

## 11. Packing Method

----TBD